IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re U.S. Patent Application of:)

Applicant(s): Thomas Michael Sailer)

Serial No.: 10/763,507)

Conf. No.: Unassigned)

Filed: January 23, 2004)

For: FAULT TOLERANT COMPUTER)
CONTROLLED SYSTEM)

Art Unit: Unassigned)

Examiner: Unassigned)

I hereby certify that this paper is being deposited with the
United States Postal Service as FIRST-CLASS mail in an
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EB. 19, 2004

Date

Registration No. 30,270
(Attorney for Applicant(s))INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This IDS is submitted under 37 C.F.R. § 1.97(b) within either of the following time periods, whichever occurs last:

- (a) within three months of either the filing date of the national application or the date of entry into the national stage; or
- (b) before the mailing date of first office action on the merits (i.e., not including actions such as restriction requirements).

Applicant(s) submit herewith Form PTO-1449 (Information Disclosure Citation) together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 C.F.R. § 1.56. Applicant(s) respectfully submit that the citation of any reference on Form PTO-1449 does not constitute an admission that the reference qualifies as prior art.

It is requested that the information disclosed on the enclosed Form PTO-1449 be made of record in this application.

The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§ 1.16-1.17, or to credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

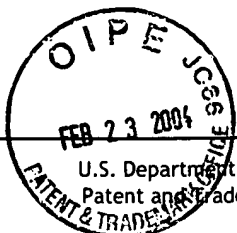
GREER, BURNS & CRAIN, LTD.

By: 

Paul G. Juettner

Registration No. 30,270

300 South Wacker Drive – Suite 2500
Chicago, Illinois 60606
Telephone: (312) 360-0080
Facsimile: (312) 360-9315
Customer Number 24978

Form PTO-1449
(Rev. 8-88)U.S. Department of Commerce
Patent and Trademark OfficeAttorney Docket No.
0517.69179

Serial No. 10/763,507

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Applicant: Thomas Michael Sailer

Filing Date: January 23, 2004

Group No. Unassigned

U.S. PATENT DOCUMENTS

Examiner initial*	Document No.	Date	Name	Class	Subclass	Filing Date
	4 6 4 4 5 3 8	February 17, 1987	Cooper et al.			June 16, 1982

FOREIGN PATENT DOCUMENTS

Document No.	Date	Country	Class	Subclass	Translation
					Yes No
0 1 3 0 6 5 2 A 1	May 3, 2001	PCT			Yes

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

L. Lamport et al, The Byzantine Generals Problem, ACM Transactions on Programming Languages and Systems, Vol. 4, No. 3, July 1982, pp. 382-401
F.B.Schneider, Understanding Protocols for Byzantine Clock Synchronization, place of publication unknown, dated August 1987.
J. Lala, A Byzantine Resilient Fault Tolerant Computer for Nuclear Power Plant Application, International Symposium on Fault Tolerant Computing Systems, Vienna, July 1-4, 1986.
K. Echtle, Fault-Masking with Reduced Redundant Communication, International Symposium on Fault Tolerant Computing Systems, Vienna, July 1-4, 1986.

Examiner

Date Considered

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.